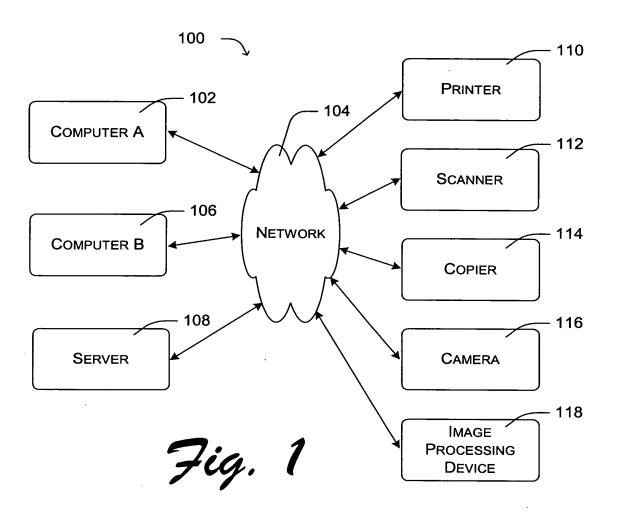
PDNO: 10004288-1

Inventor: Roylance, Eugene A.

Title: Configurable Image Processing Logic
For Use In Image Processing Devices



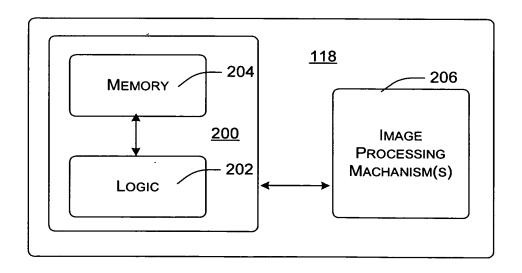


Fig. 2



PDNO: 10004288-1 Inventor: Roylance, Eugene A. Title: Configurable Image Processing Logic For Use In Image Processing Devices



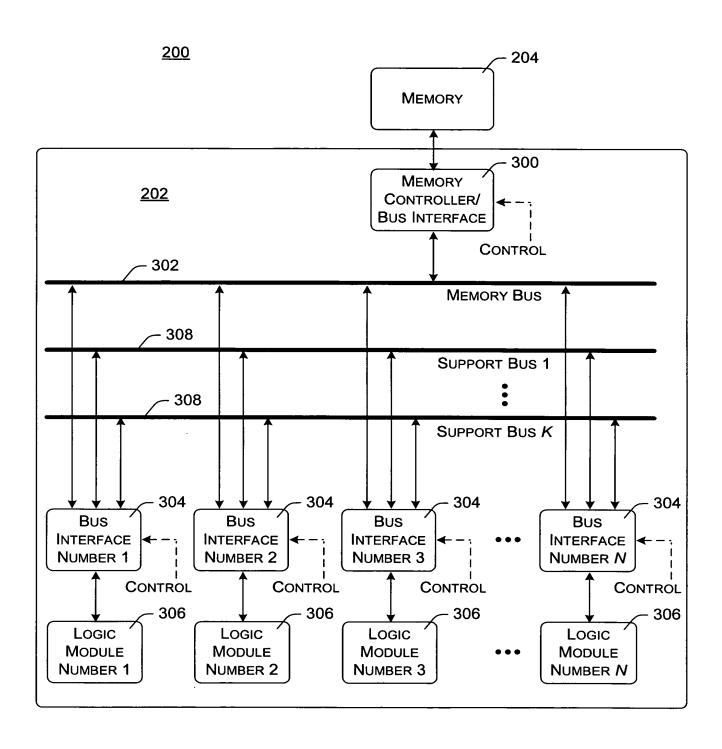


Fig. 3



PDNO: 10004288-1

Inventor: Roylance, Eugene A.

Title: Configurable Image Processing Logic For Use In Image Processing Devices



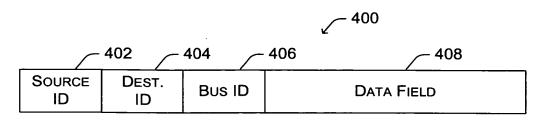


Fig. 4

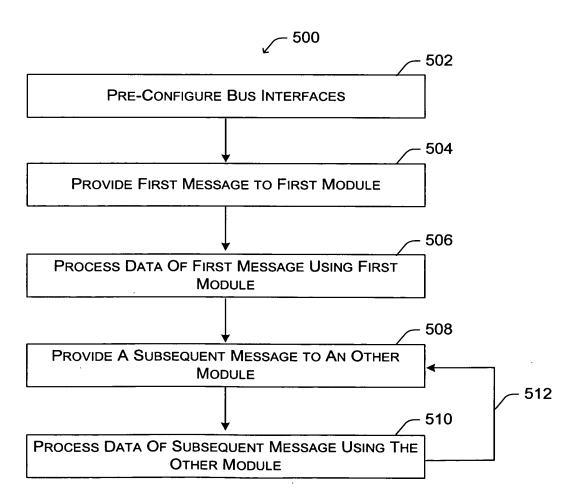


Fig. 5